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haynesboone

Haynes and Boone, LLP Attorneys and Counselors 2505 N Plano Road, Suite 4000 Richardson, Texas 75082-4101

> Phone: (972) 680-7550 Fax: (972) 680-7551 www.haynesboone.com

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To: U.S. Patent Office

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From:

Karen Underwood

Direct Telephone:

972-739-8656

Direct Fax:

972-692-9056

Should you have any problem with this transmission, please call: 972-739-8656

Message:

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Attorney Docket Number: 24061.39 / 2003-0030

Customer No. 42717

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:		§	
	Jhon Jhy Liaw	§ §	Confirmation No.: 8019
Serial No.: 10/687,424		§ §	Group Art Unit: 1765
Filed:	October 16, 2003	ş	Examiner: Kin Chan Chen
For:	Process Integration of SOI FETs with Active Layer Spacer	8 8	 श्रमक्ष्ण के क्रुब उन्

Mail Stop: AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 BEST AVAILABLE COPY

RESPONSE TO NON-FINAL OFFICE ACTION MAILED JANUARY 19, 2006

The present paper is submitted in response to the Non-Final Office Action of January 19, 2006. No fees, including extension of time fees, are believed necessary for consideration of the present paper. However, if other fees are necessary, including extension of time fees, the extension of time is hereby requested, and the Commissioner is hereby authorized to charge any fees, including those for the extension of time, to Haynes and Boone, LLP's Deposit Account No. 08-1394.

Amendments to the Claims are reflected in the listing of claims beginning on page 2 of this paper. Remarks/Arguments begin on page 7 of this paper.